

REMARKS

Claims 2-9 and 11-28 are pending in the application.

Claims 27-28 are new.

Claims 11-14 and 23-26 are allowed.

Claims 2-5 and 15-22 are rejected.

Claims 6-9 are objected to.

Claims 2-5 and 15-22 are rejected under 35 U.S.C. 102(e).

Claim Rejections - 35 U.S.C. § 102

Claims 2-5 and 15-22 are rejected under 35 U.S.C. 102(e) as being anticipated by Kurihara, et al. (US Patent No. 6,327,194).

Each of claim 2 and claim 15 include a ready/busy driver controller configured to generate a busy enable signal in response to the power-up signal and the command busy signal. Similarly, claim 19 includes generating a busy enable signal with the ready/busy driver controller in response to the power-up signal and the at least one busy signal. Thus, both the power-up signal and the command busy signal are used to generate the busy enable signal. However, in Kurihara, none of the references to the ready/busy output 130 mention anything about a signal from the VCC detector 106, which the Examiner cited as the voltage level detector. The Vcc detector 106 signals the state control and command register 102 when proper Vcc is detected. *Kurihara, col. 6, ll. 48-52.* The ready/busy output 130 indicates when the device is busy undergoing an embedded operation. The embedded operations are program and erase operations. *Kurihara, col. 5, ll. 48-50 and 62-65.* Thus, the ready/busy output 130 indicates only program and erase operations. Nothing else is mentioned concerning the ready/busy output 130, in particular, nothing regarding a signal from the VCC detector 106. Thus, there is no ready busy signal described in Kurihara that is generated in response to a power-up signal. As a result, Kurihara does not teach each and every element of claims 2, 15, 19, and dependent claims 3-5, 16-18 and 20-22. The Applicant requests that the Examiner withdraw the rejections of claims 2-5 and 15-22.

Claim 27 recites the voltage is an internal voltage generated internal to the semiconductor device; and the minimum voltage is a minimum internal voltage required to operate the device. Claim 28 includes similar elements. Kurihara does disclose voltage generators 104 and 112,

however, no voltage level detector, including the VCC detector 106 is described as generating a power-up signal if the internal voltage is less than a minimum internal voltage required to operate the device. As a result, Kurihara does not teach each and every element of claim 27.

For the foregoing reasons, reconsideration and allowance of claims 2-9 and 11-28 of the application as amended is requested. The Examiner is encouraged to telephone the undersigned at (503) 222-3613 if it appears that an interview would be helpful in advancing the case.

Respectfully submitted,

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